

WHAT IS CLAIMED IS:

1. A liquid crystal display device comprising:  
a first substrate;  
a second substrate having a face opposing to the  
5 first substrate;  
a frame-shaped sealing member which bonds the  
first and second substrates to each other;  
liquid crystal which is sealed in a space defined  
by the first and second substrates and the sealing  
10 member; and  
a plurality of first wires, a plurality of second  
wires formed in a direction perpendicular to the first  
wires, and a plurality of leading wires which extend  
generally in parallel to the first wires and which are  
15 respectively connected to corresponding one of the  
second wires,  
wherein the sealing member has a first side  
portion extending generally in parallel to the first  
wires, the plurality of leading wires extend along the  
20 first side portion of the sealing member, and partial  
lines of the leading lines are formed on an area  
overlapping on the first side portion of the sealing  
member.
2. A liquid crystal display device according to  
25 claim 1, wherein the second substrate has one side  
portion which is not opposed to the first substrate and  
an integrated circuit connected to the plurality of

3. A liquid crystal display device according to claim 1, wherein a frame-shaped light shielding film defining a display area is formed on the first substrate, and the light shielding film is formed so as not to overlap on the first side portion of the sealing member.

5. A liquid crystal display device according to claim 1, further comprising a sealing member diffusion preventing wall provided outside the sealing member.

7. A liquid crystal display device according to claim 1, wherein the sealing member has a second side portion which is opposed to the first side portion and which is generally parallel to the first side portion, partial lines of the plurality of leading wires extend  
25 along the second side portion of the sealing member, and at least partial wires thereof are formed on an

area overlapping the second side portion of the sealing member.

8. A liquid crystal display device according to claim 7, wherein the second substrate has one side portion which is not opposed to the first substrate, and a semiconductor integrated circuit connected to the plurality of first lines and the plurality of leading lines are mounted on the one side portion.

9. A liquid crystal display device, comprising:  
an active substrate provided with a plurality of scanning signal lines, a plurality of data signal lines formed in a direction perpendicular to the scanning signal lines, and a plurality of leading lines connected to corresponding one of the scanning signal lines;

an opposing substrate having a face opposed to the active substrate;

a frame-shaped sealing member which bonds the active substrate and the opposing substrate;

liquid crystal disposed inside a space defined by the active substrate, the opposing substrate and the sealing member; and

a frame-shaped light shielding film which is formed on the opposing substrate and defines a display area,

wherein the sealing member has a first side portion which is generally perpendicular to the

scanning signal lines,

the leading lines are formed along the first side portion of the sealing member, and

at least an area of the light shielding film which  
5 corresponds to the first side portion of the sealing member is formed inside the first side portion of the sealing member so as to have a gap from the first side portion of the sealing member.

10 10. A liquid crystal display device according to claim 9, wherein the active substrate has one side portion which is not opposed to the opposing substrate, and a semiconductor integrated circuit connected to the plurality of data signal lines and the plurality of leading lines is mounted on the one side portion.

15 11. A liquid crystal display device according to claim 9, wherein an area of the light shielding film which corresponds to the first side portion of the sealing member is formed at a position spaced from the first side portion of the sealing member by a distance  
20 of 0.2 mm or less.

12. A liquid crystal display device according to claim 9, wherein partial lines of the leading lines are formed on an area overlapping on the first side portion of the sealing member.

25 13. A liquid crystal display device according to claim 9, wherein the sealing member has a second side portion which is provided on a side opposed to the

first side portion and which is generally parallel to the first side portion, and partial lines of the plurality of leading lines extend along the second side portion and other partial lines thereof are formed on an area overlapping on the second side portion of the sealing member.

14. A liquid crystal display device, comprising:  
an opposing substrate;

an active substrate which is provided with a plurality of scanning signal lines, a plurality of data signal lines formed in a direction perpendicular to the scanning signal lines, and a plurality of leading lines connected to corresponding one of the scanning signal lines, and which has a face opposed to the opposing substrate and an one-side portion which is not opposed to the opposing substrate;

a frame-shaped sealing member which bonds the active substrate and the opposing substrate;

liquid crystal disposed inside a space defined by the active substrate, the opposing substrate and the sealing member; and

a frame-shaped light shielding film which is formed on the opposing substrate and defines a display area,

wherein the sealing member has a pair of side portions which are generally perpendicular to the scanning signal lines, respectively,

the leading lines are formed along respective side portions of the sealing member, and at least partial lines thereof are formed on an area overlapping on one of the side portions of the sealing member, and

5           a semiconductor integrated circuit connected to the data signal lines and the leading lines is mounted on the one-side portion of the active substrate.

15. A liquid crystal display device, comprising:

an opposing substrate;

10           an active substrate which is provided with a plurality of scanning signal lines, a plurality of data signal lines formed in a direction perpendicular to the scanning signal lines, and a plurality of leading lines connected to corresponding scanning signal lines, and  
15           which has a face which is opposed to the opposing substrate, a first side portion which is not opposed to the opposing substrate, and a second side portion which is opposed to the first side portion;

20           a frame-shaped sealing member which bonds the active substrate and the opposing substrate;

liquid crystal disposed inside a space defined by the active substrate, the opposing substrate and the sealing member;

25           a frame-shaped light shielding film which is formed on the opposing substrate and defines a display area;

a first semiconductor integrated circuit which is

a second semiconductor integrated circuit which is  
5 mounted on the second side portion of the active  
substrate and which is connected to the leading lines,

10           the leading lines are formed along respective side portions of the sealing member, and at least partial lines thereof are formed on an area overlapping on one side portion of the sealing member.

```

15         an opposing substrate;

```

25           a frame-shaped sealing member which bonds the  
active substrate and the opposing substrate;

liquid crystal disposed inside a space defined by

the active substrate, the opposing substrate and the sealing member;

a frame-shaped light shielding film which is formed on the opposing substrate and defines a display area;

a first semiconductor integrated circuit which is mounted on the first side portion of the active substrate and which is connected to the data signal lines;

a second semiconductor integrated circuit which is mounted on the first side portion of the active substrate and which is connected to partial lines of the leading lines; and

a third semiconductor integrated circuit which is mounted on the second side portion of the active substrate and which is connected to the other partial lines of the leading lines,

wherein the sealing member has a pair of side portions which are generally perpendicular to the scanning signal lines, respectively,

the leading lines connected to the second semiconductor integrated circuit are formed along respective side portions of the sealing member, at least partial lines thereof are formed on an area overlapping on one side portion of the sealing member, the leading lines connected to the third semiconductor integrated circuit are formed along respective side



portions of the sealing member and at least partial lines thereof are formed on an area overlapping on one side portion of the sealing member.

RECEIVED